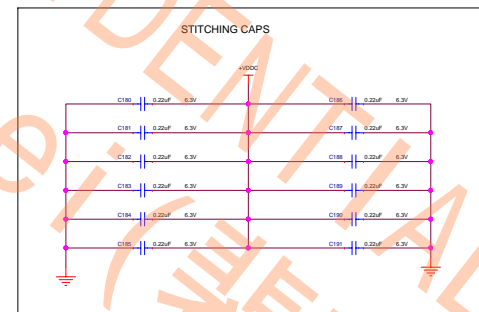
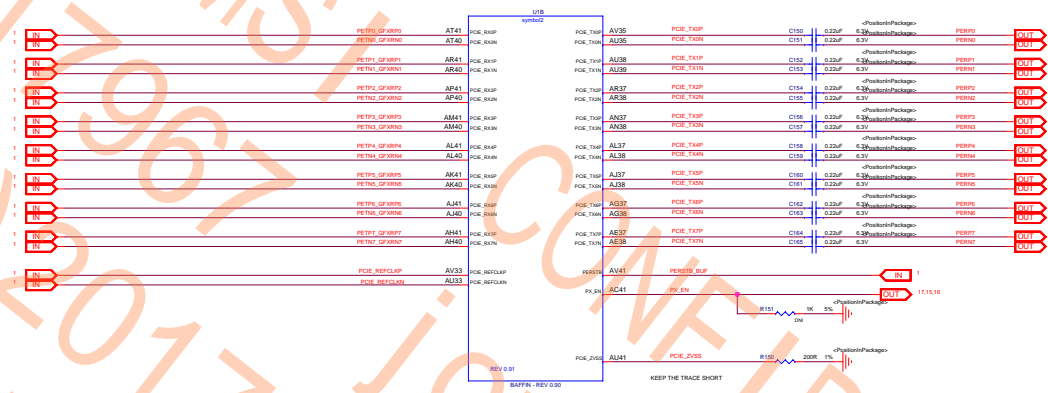


(2) BAFFIN PCIE



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: BAFFIN PCIE

DATE: Wed Jun 01 06:00:28 2016

REV: 1.0

SHEET NUMBER: 2 OF 19

DOCUMENT NUMBER: 105_CXXX00_00A

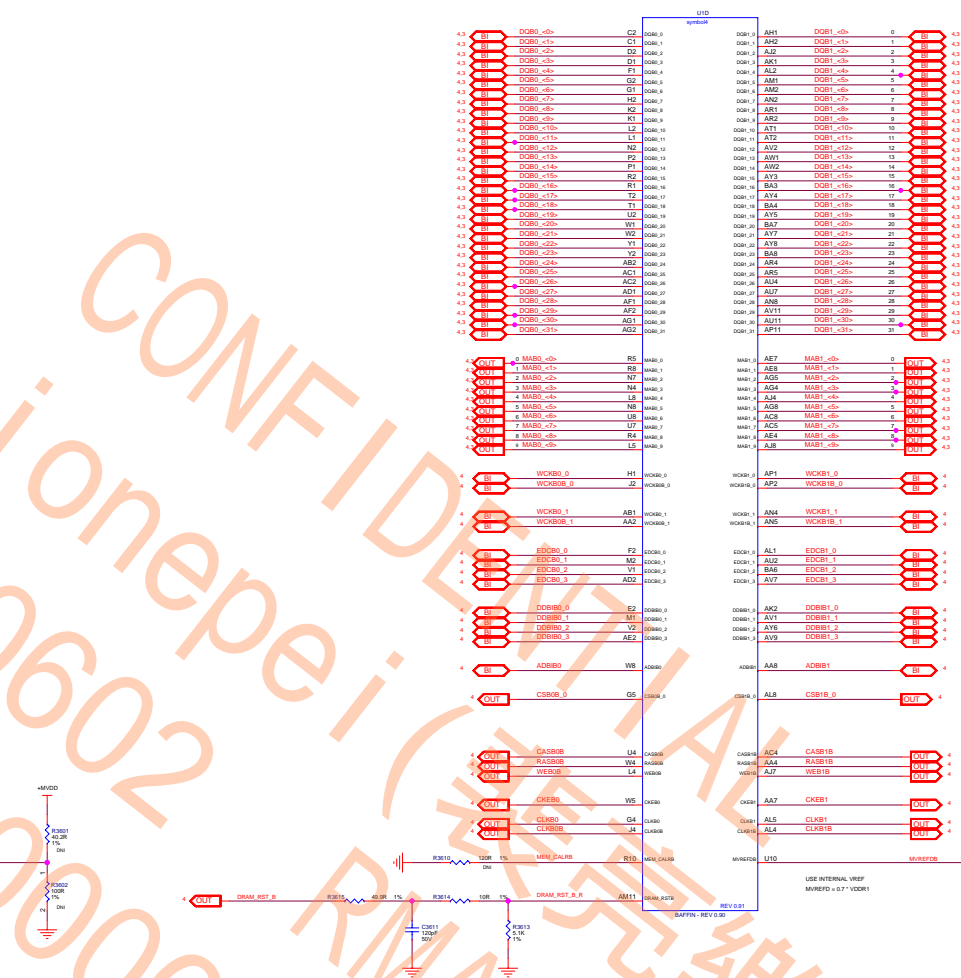
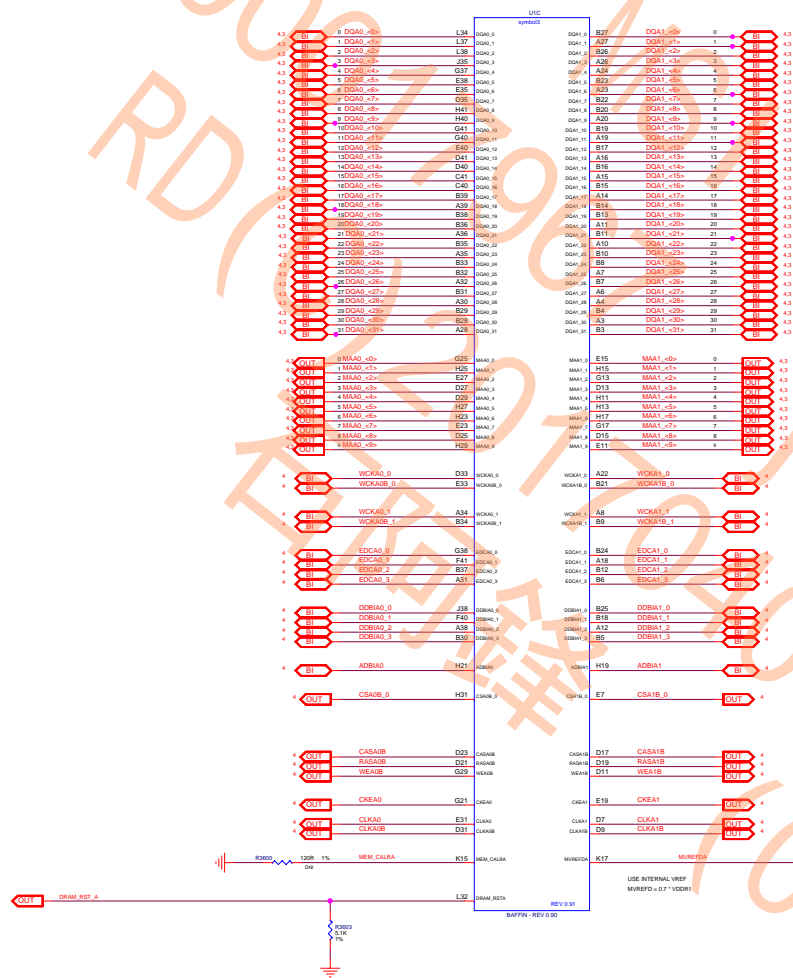
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TITLE:	TITLE
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(3) RACERX MEMORY INTERFACE



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: BAFFIN MEMORY

DATE: Wed Jun 01 06:00:28 2016

SHEET NUMBER: 3 OF 11

DOCUMENT NUMBER: 105_CXXX00_00A

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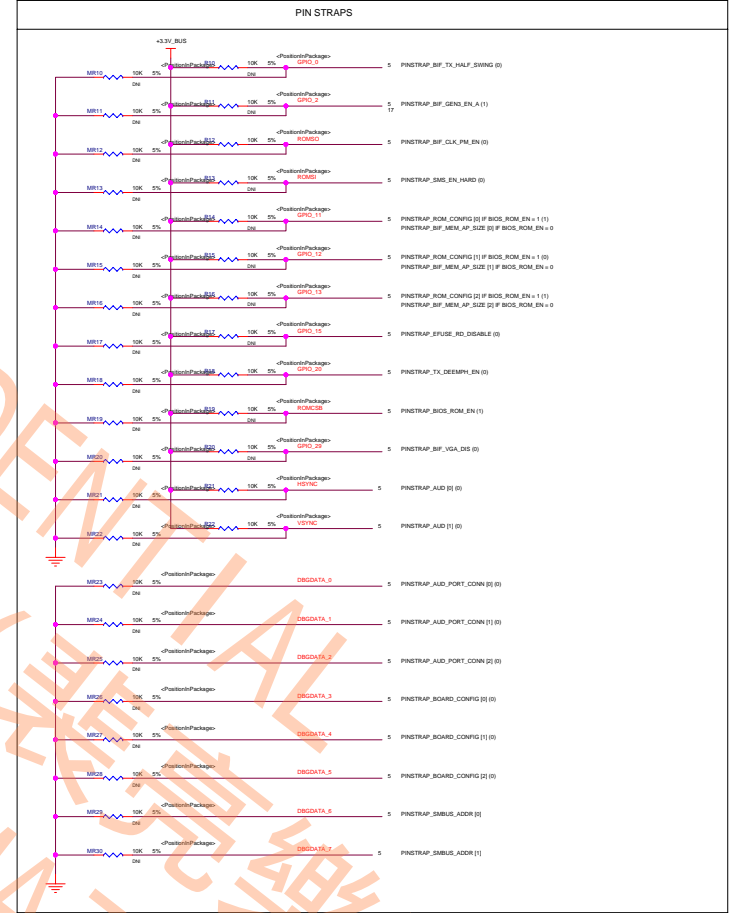
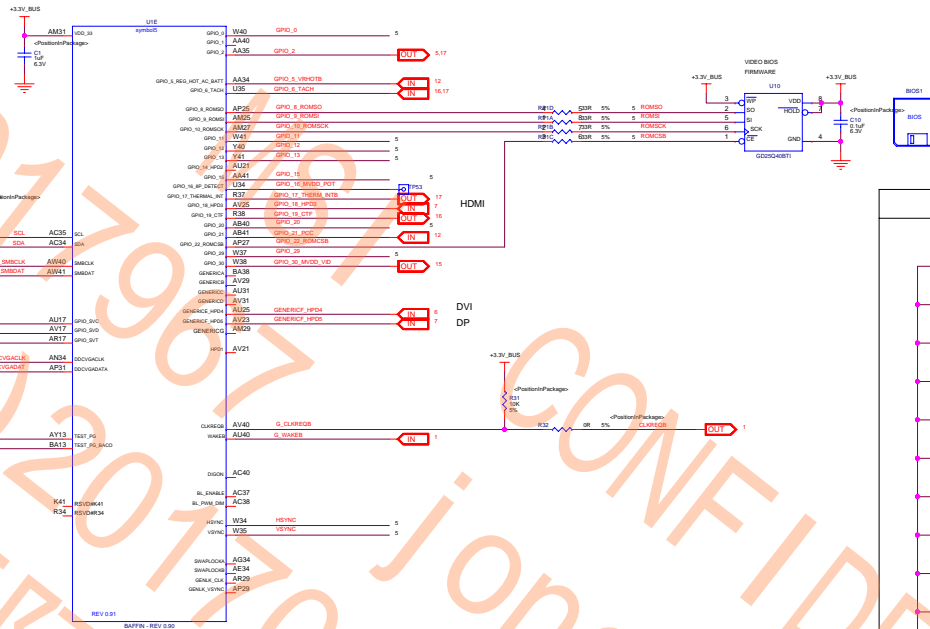
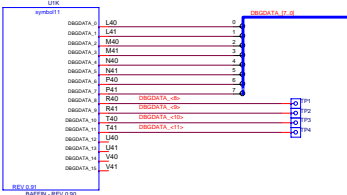
MF :



(5) BAFFIN GPIO STRAPS CLK

DEVICE	ADDRESS
AD974	0x0
RT4953	0x0
VT8851	

DEVICE	ADDRESS
LM59693	0x0



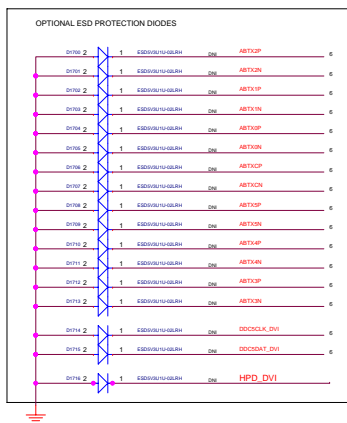
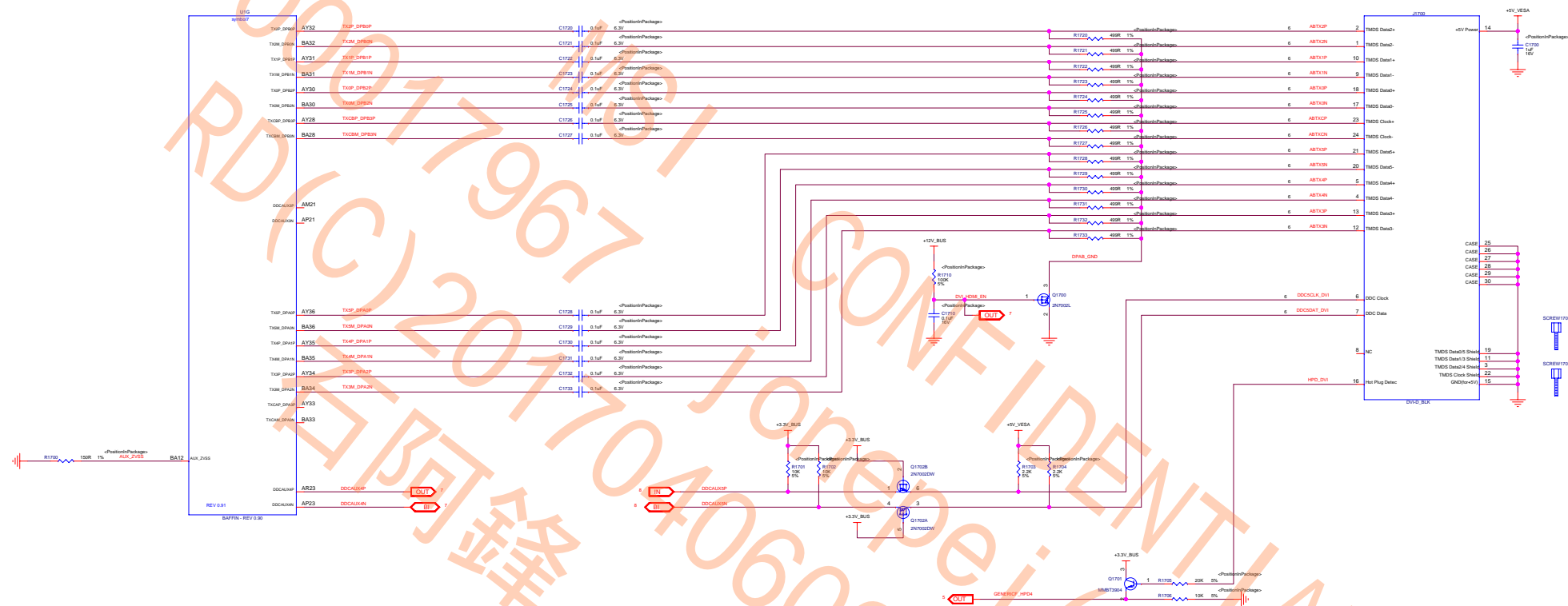
AMD - PLATFORM HARDWARE ENG
#48, No.1367, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET:	BAFFIN GPIO STRAPS CLK
DATE:	Wed Jun 01 06:00:29 2016
SHEET NUMBER:	5 OF 19
DOCUMENT NUMBER:	106_CXX000_00A

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TITLE:	TITLE
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(6) BAFFIN TMDPAB - dDVI



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: BAFFIN TMDPAB - dDVI

DATE: Wed Jun 01 06:00:29 2016

SHEET NUMBER: 6 OF

DOCUMENT NUMBER: 105_CXXX00_00

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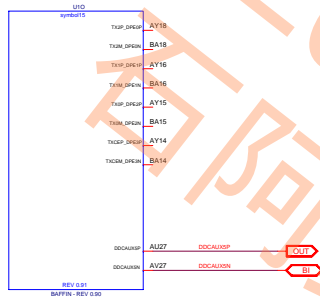
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TITLE

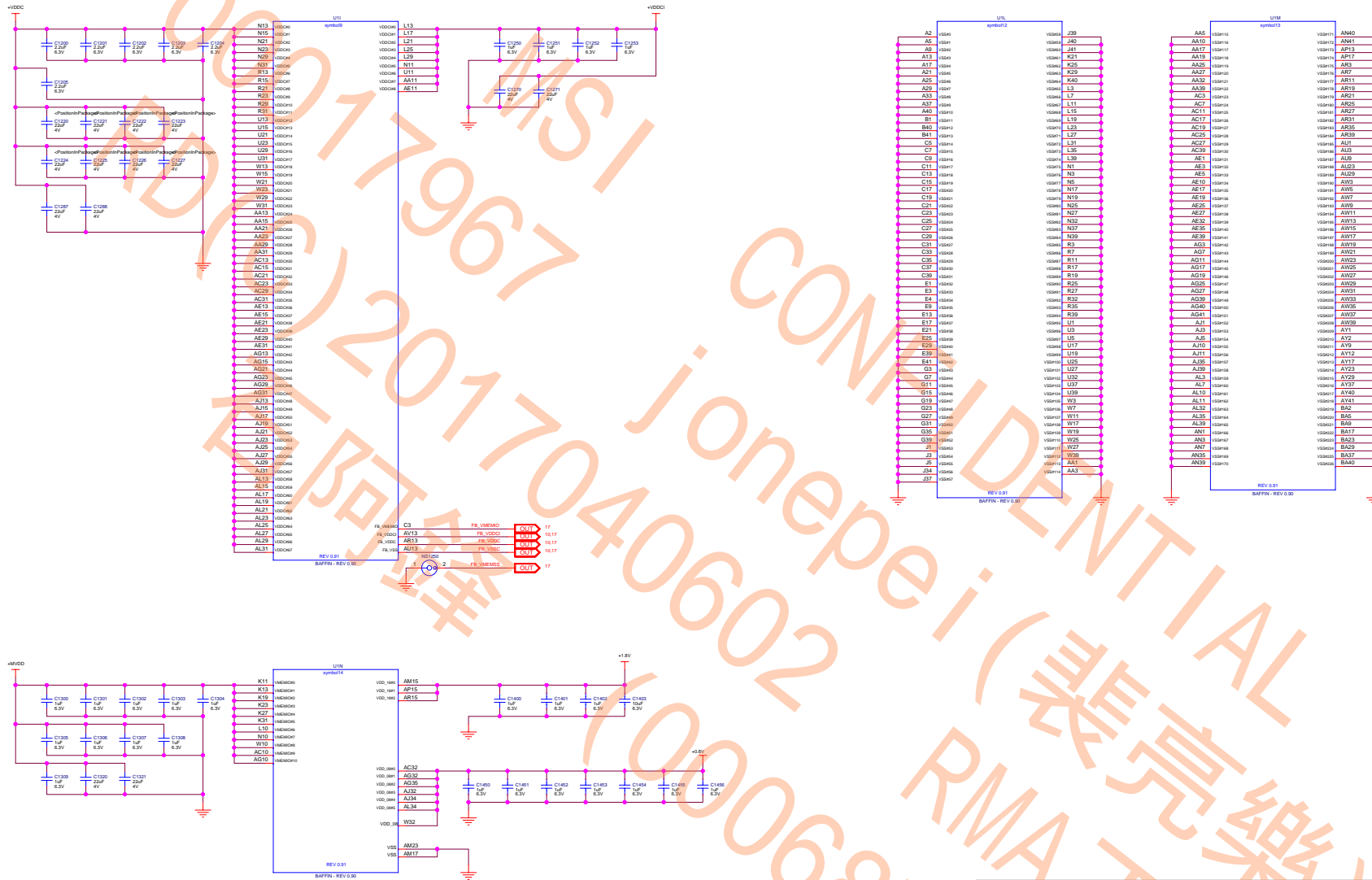
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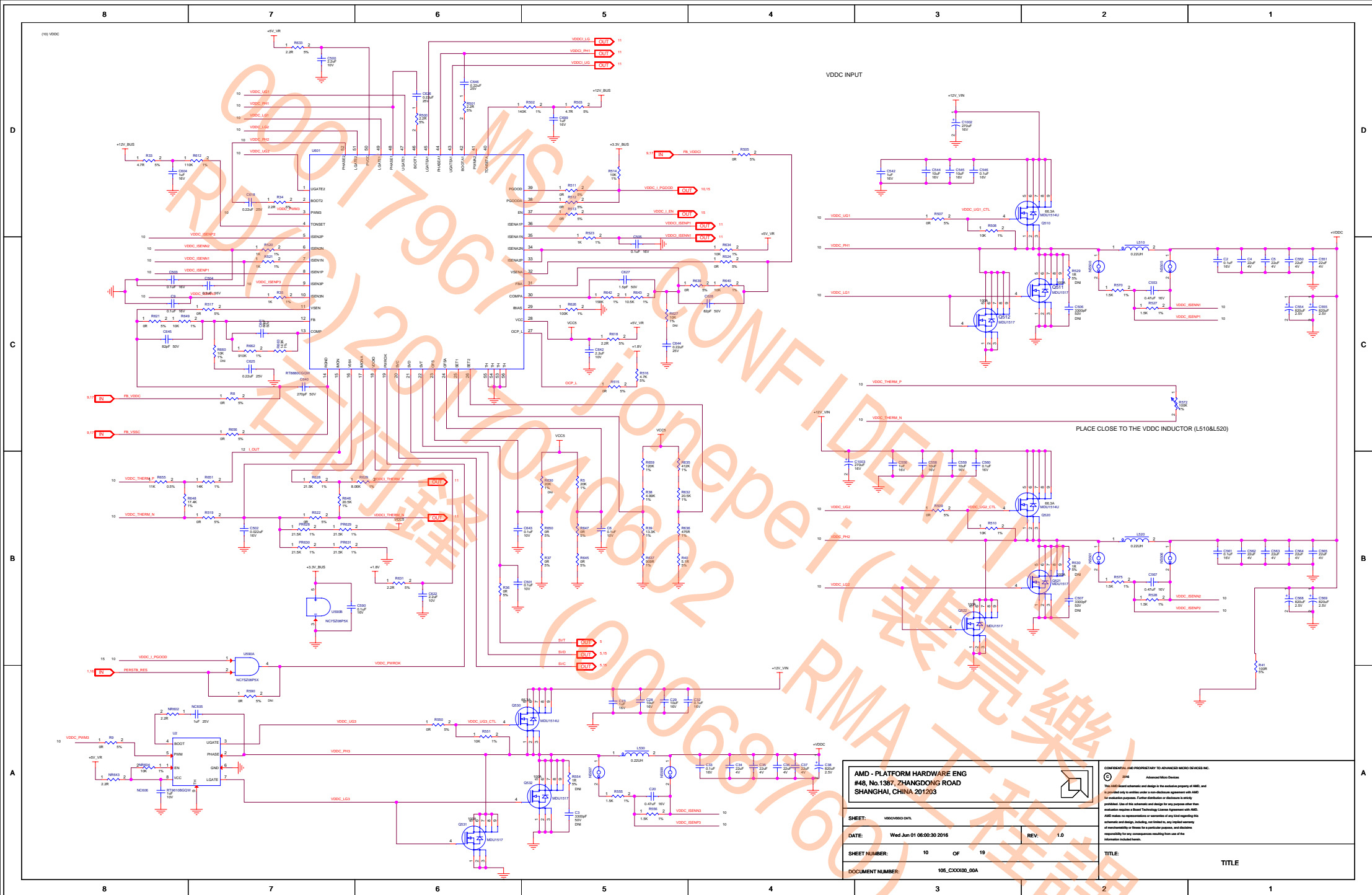
(8) BAFFIN TMDPE



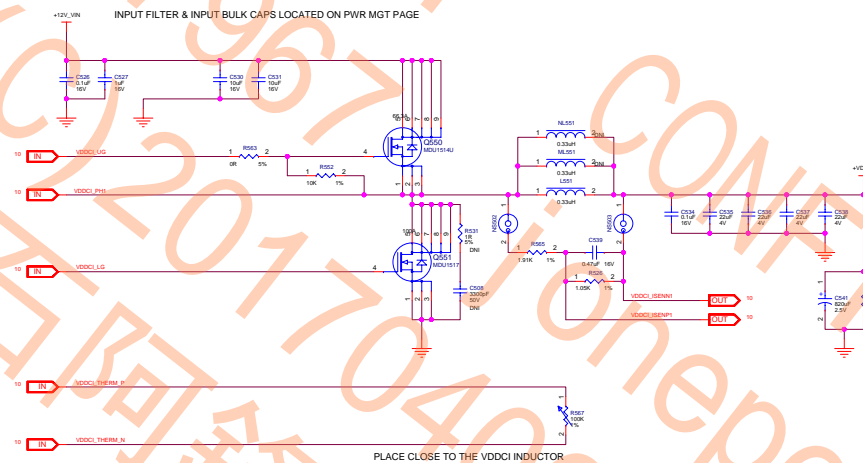
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SHEET: BAFFIN TMDPE - NC		DATE: Wed Jun 01 06:00:29 2016		REV: 1.0	
SHEET NUMBER: 8 OF 19		TITLE:			
DOCUMENT NUMBER: 106_CXXX00_00A		TITLE			


(9) BAFFIN POWER GND



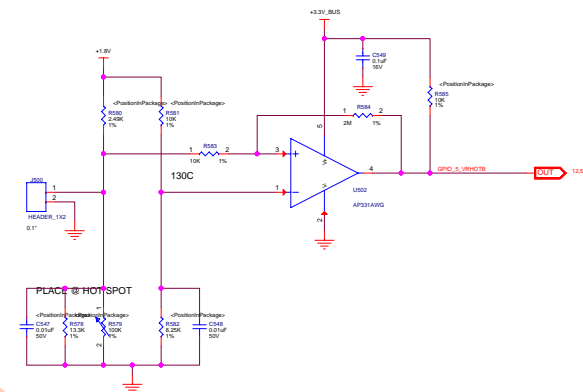


(11) VDDCI

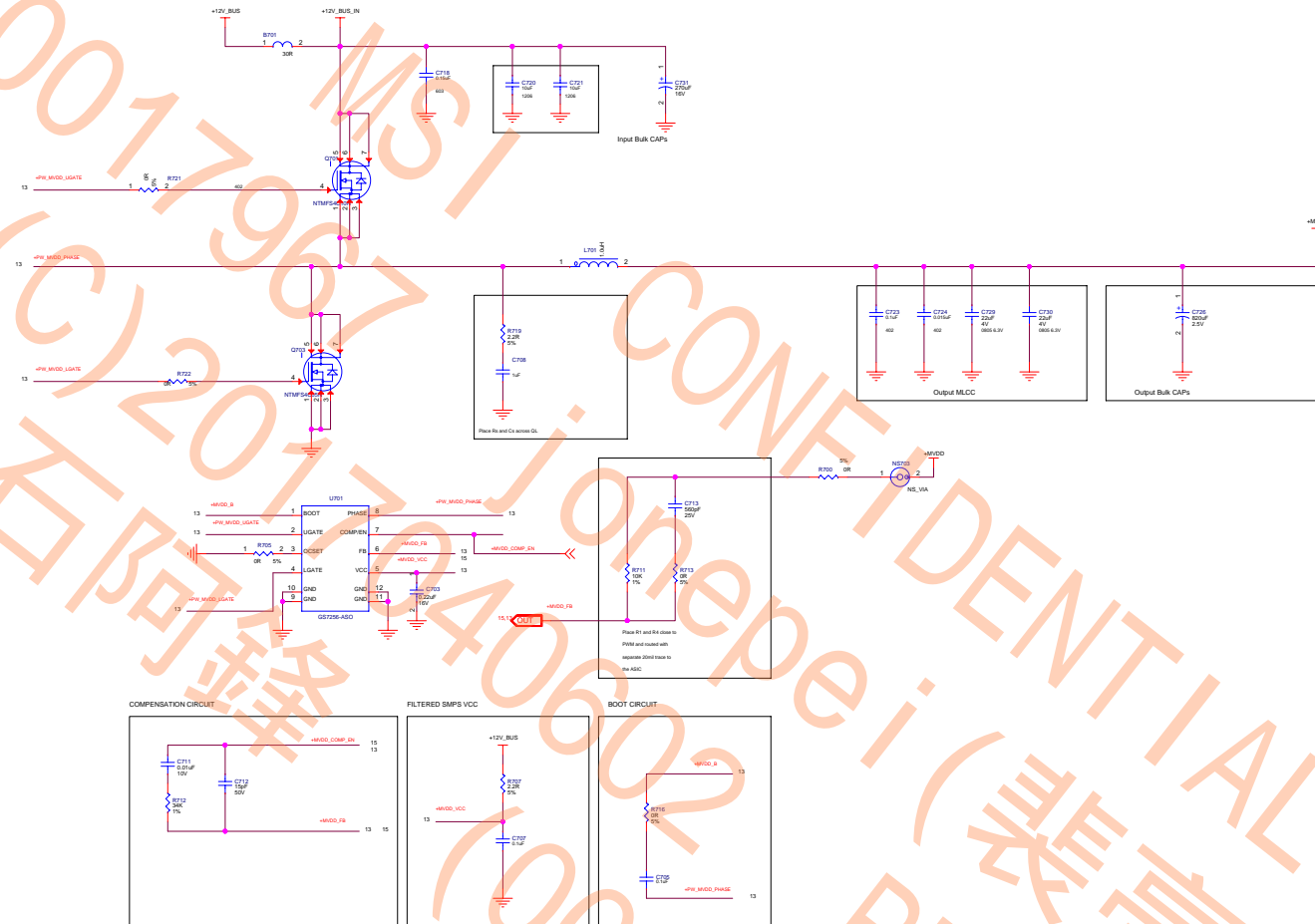



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SHEET: VDDCI			REV: 1.0			TITLE: TITLE		
DATE: Wed Jun 01 06:00:30 2016			SHEET NUMBER: 11 OF 19			DOCUMENT NUMBER: 105_CX0X00_00A		

TITLE:	TITLE
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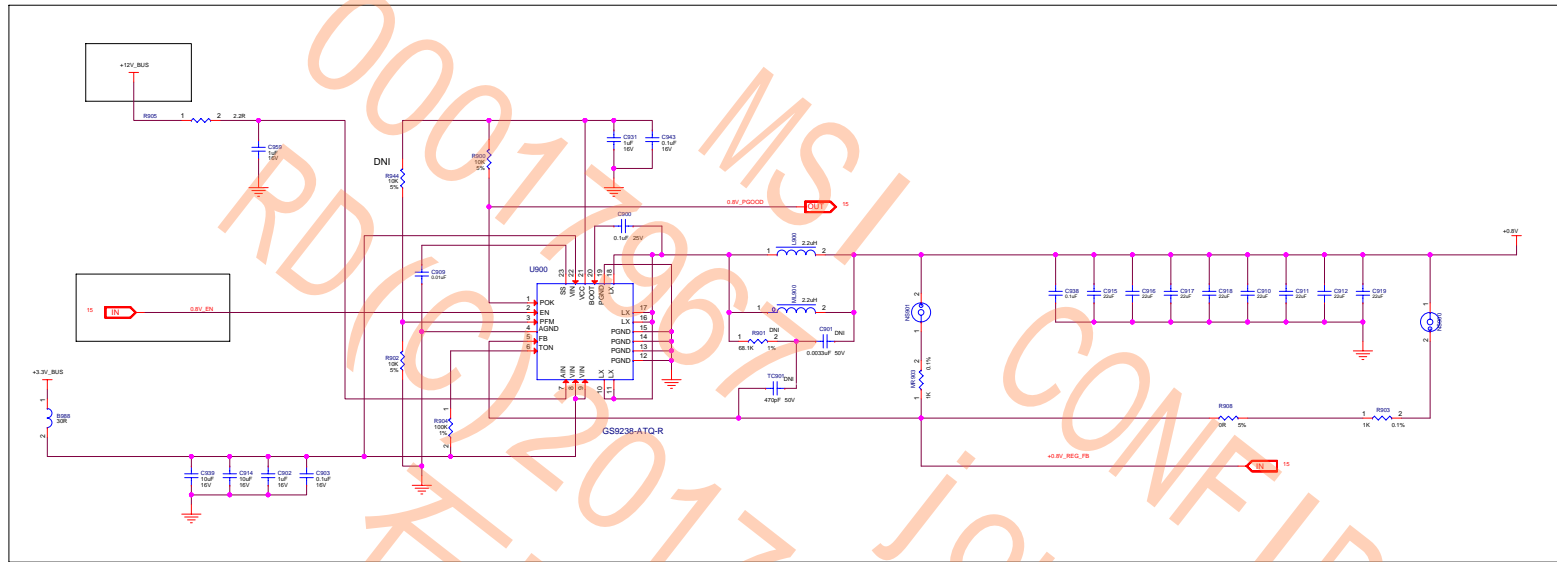
(18) MVDD



AMD - PLATFORM HARDWARE ENG #48, No.1367, ZHANGDONG ROAD SHANGHAI, CHINA 201203			
SHEET: MVDD		REV: 1.0	
DATE: Wed Jun 01 06:00:30 2016			
SHEET NUMBER: 13		OF	19
DOCUMENT NUMBER: 106_CXX000_00A			

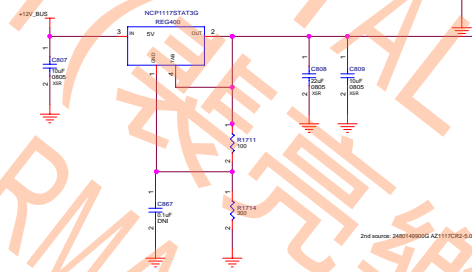
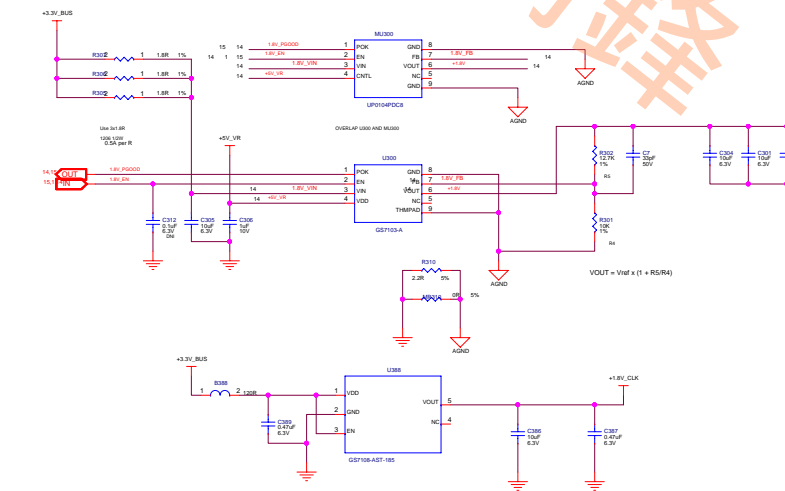
TITLE:	TITLE
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(19) 0.8V



LDO #1: VIN = 3.0V TO 3.6V MAX VOUT = +1.8V +/-2% IOUT = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

REGULATOR FOR +5V RAILS
IOUT MAX = 150mA



AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: 0.8V REG	
DATE: Wed Jun 01 06:00:30 2016	REV: 1.0
SHEET NUMBER: 14 OF 19	
DOCUMENT NUMBER: 105_CXX00_00A	
NOTES: NOTE	

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TITLE:	TITLE

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AMD				TITLE:		DOCUMENT NUMBER: 106_C00000_00A		DATE: Wed Jun 01 06:00:31 2016		SHEET NUMBER: 19 OF 19		REV: 1.0			
REVISION HISTORY				ENGINEER: XXXX		NOTES: NOTE		CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC. This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representation or warranty of any kind regarding this schematic and design, including, not limited to, any implied warranty of non-infringement or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of this information included herein.		AMD - PLATFORM HARDWARE ENG #48, No.1367, ZHANGDONG ROAD SHANGHAI, CHINA 201203					
SCH Rev		PCB Rev		Date		REVISION DESCRIPTION									
1		006		10/20/2015		Initial Design									
1		006		10/20/2015		AMD 1.01 USB to SATA Bridge, AMD 100MHz CLOCK speed Change 0.01 power good pin_06									

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